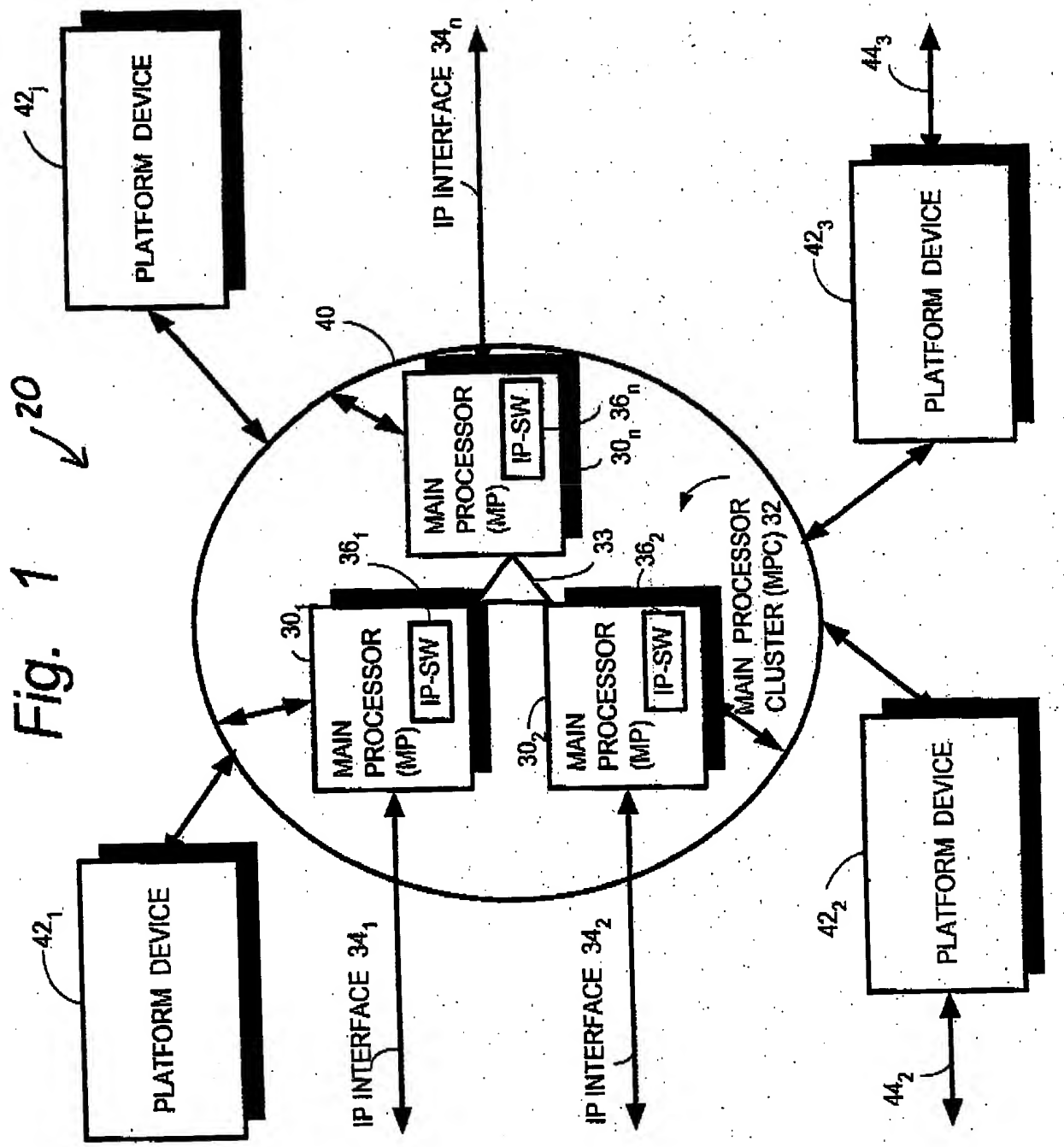
ANNOTATED SHEET

Fig. 1



ANNOTATED SHEET

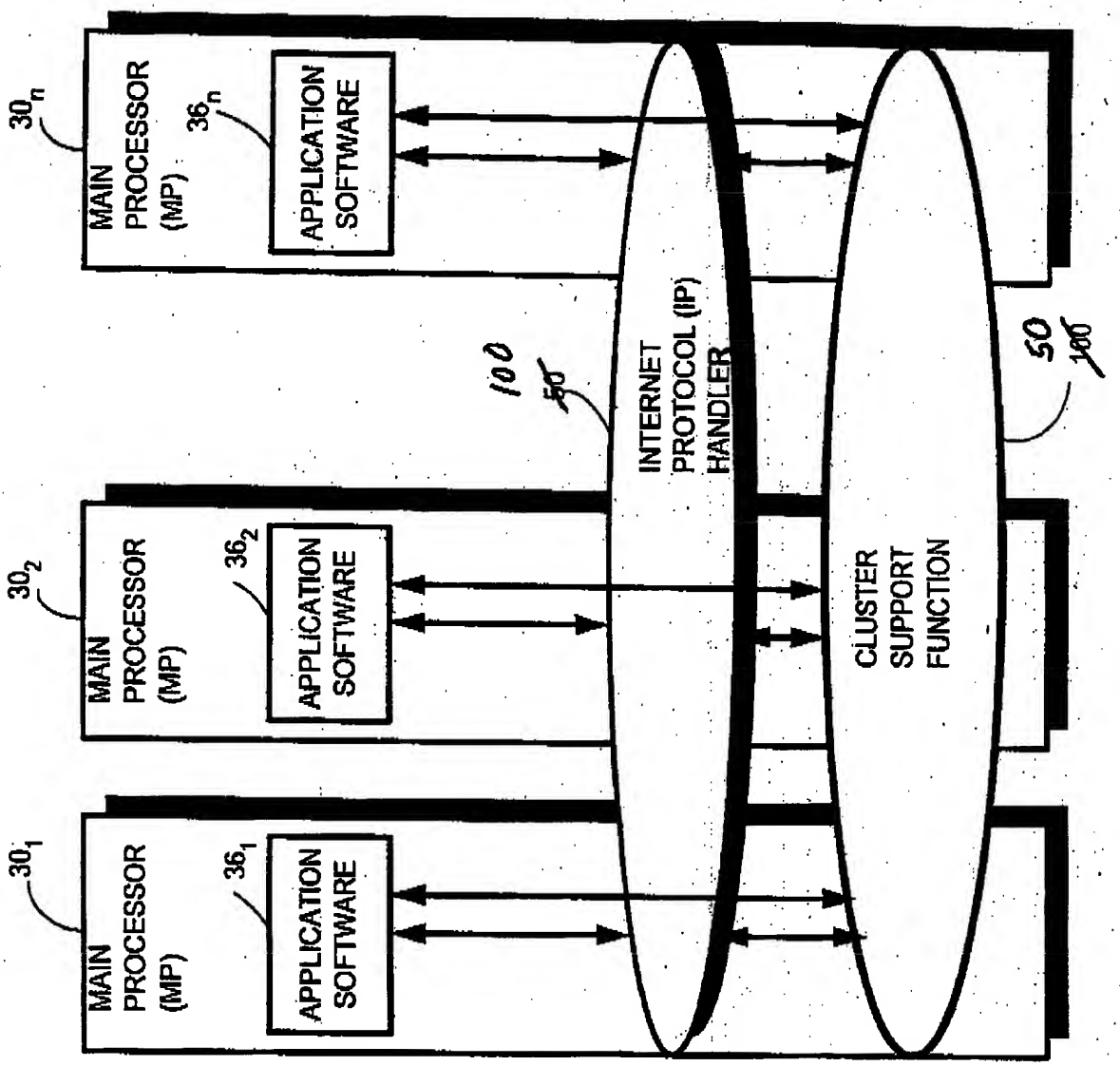
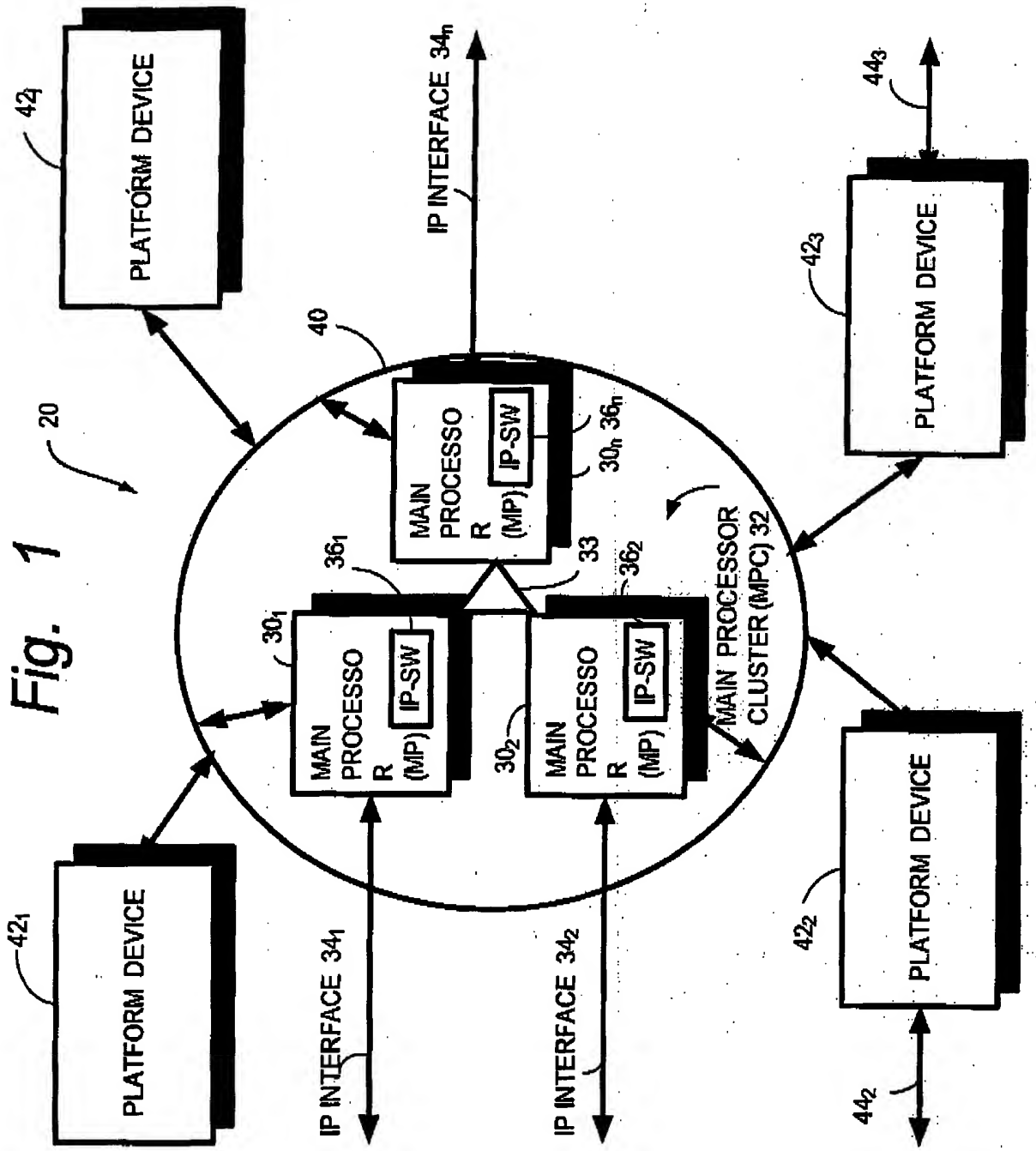


Fig. 2

Fig. 1


20
40
MAIN PROCESSOR CLUSTER (MPC) 32
MAIN PROCESSOR (MP)
IP-SW
30_1
30_2
30_n
36_1
36_2
36_n
33
IP INTERFACE 34_1
IP INTERFACE 34_2
IP INTERFACE 34_n
PLATFORM DEVICE
42_1
42_2
42_3
42_4
44_2
44_3

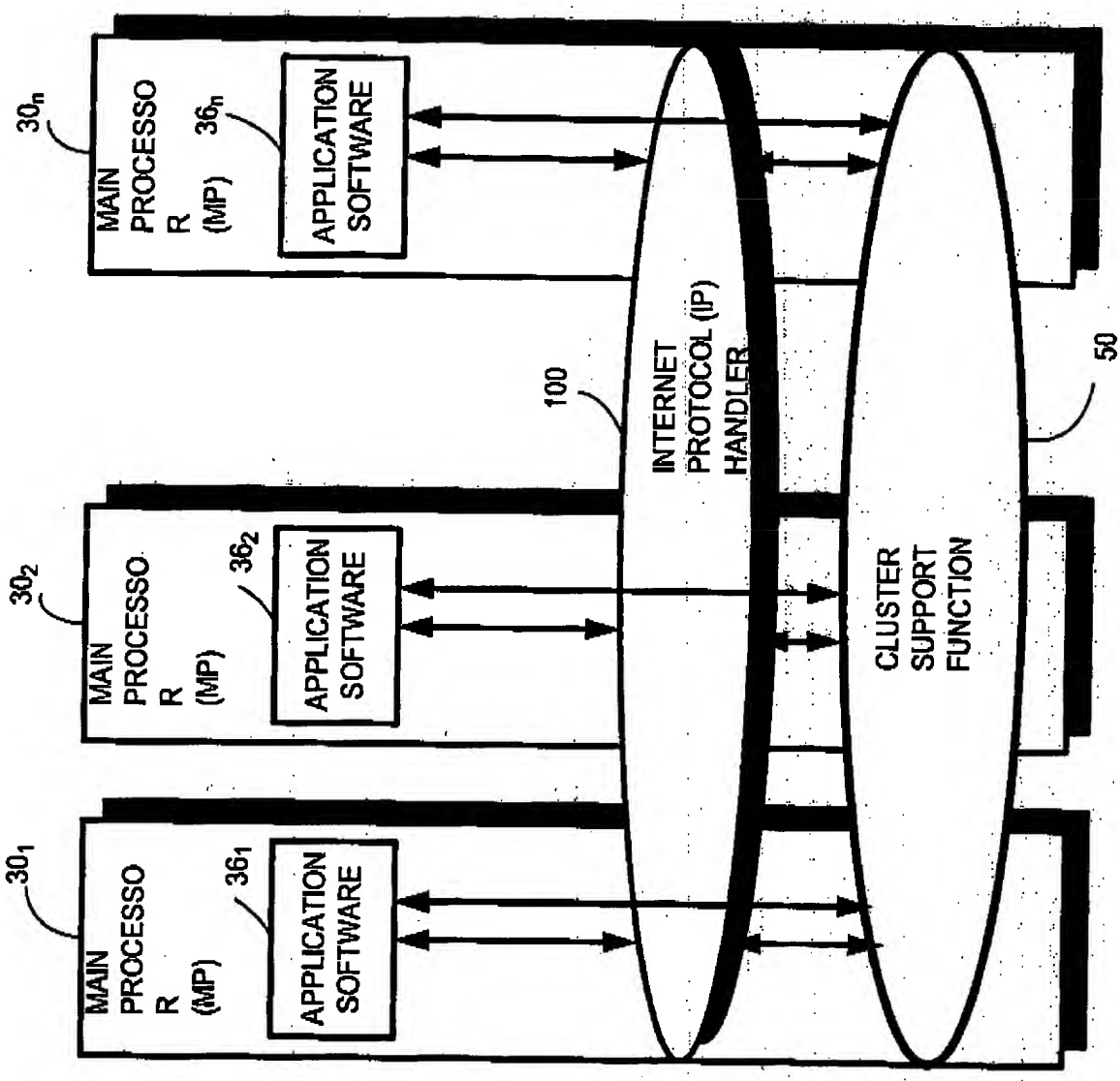

MAIN PROCESSOR (MP)
APPLICATION SOFTWARE
30_1
36_1
30_2
36_2
30_n
36_n
INTERNET PROTOCOL (IP) HANDLER
100
CLUSTER SUPPORT FUNCTION
50

Fig. 2